Electronic Acknowledgement Receipt				
EFS ID:	1150899			
Application Number:	10663144			
Confirmation Number:	2156			
Title of Invention:	Method of operating a memory at high speed using a cycle ready status output signal			
First Named Inventor:	Bryan D. Sheffield			
Customer Number:	23494			
Filer:	Alan K. Stewart/Minerva Aponte			
Filer Authorized By:	Alan K. Stewart			
Attorney Docket Number:	TI-35286			
Receipt Date:	11-AUG-2006			
Filing Date:	16-SEP-2003			
Time Stamp:	12:58:02			
Application Type:	Utility			
International Application Number:				

## Payment information:

Submitted with Payment no	Submitted with Payment	no
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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		T35286_amd2.pdf	8993	yes	3

	Multipart Description			
	Doc Desc	Start	End	
	Amendment - After Non-Final Rejection	1	1	
	Applicant Arguments/Remarks Made in an Amendment	2	3	
Warnings:				

Information:

Total Files Size (in bytes):

8993

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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.